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To:	Examiner Christi	an Chase	From:	Fred G. Pruner,	Jr.
Company:	U.S. Patent & Trademark Office		Dates	August 22, 2005	
Fax:	(571) 273-8300		Pages	5 (including coversheet)	
Your Re:	10/038,960		Our Re:	ITL.0668US (P12986)	
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MESSAGE:

Applicant: Jeffrey R. Wilcox et al.

Serial No.: 10/038,960 Filing Date: January 2, 2002

Title: Method And Apparatus For Reducing Power Consumption In A

Memory Bus Interface By Selectively Disabling And Enabling Sense

Amplifiers

Reply to Office Action dated June 22, 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Jeffrey R. Wilcox, et al.

Group Art Unit:

2187

Serial No.:

10/038,960

Examiner:

Christian Chase

Filed:

January 2, 2002

Assignee:

Intel Corporation

For:

Method and Apparatus For

<u>aaaaaaaaaa</u> **Reducing Power Consumption**

§ In A Memory Bus Interface By Selectively Disabling And **Enabling Sense Amplifiers**

Atty. Dkt. No.:

ITL.0668US (P12986)

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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August 22, 2005 Date of Deposit: I certify that this document and authorization to charge deposit account is being faceitnile transmitted to the United States Patent and Trademark 571/273-83(0) of the date indicated above. Office (Fax N

Janice Muno